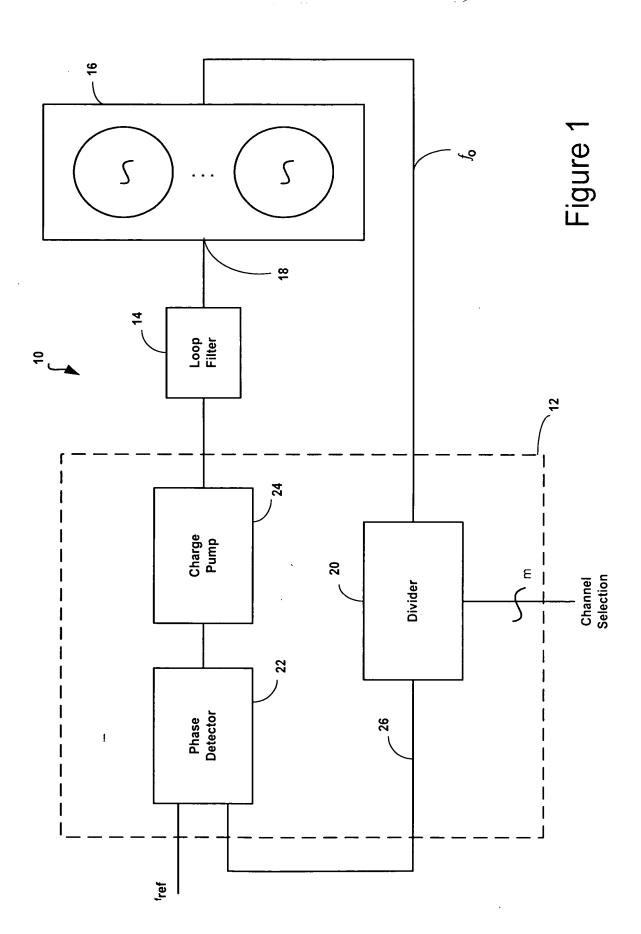
Sheet 1 of 7 Fig. 1
Inventor: Molnar, et al.
Filed: HEREWITH, November 13, 2003
Title: ON-CHIP VCO CALIBRATION
Mintz, Levin, Cohn, Ferris, Glovsky & Popeo
Telephone: (617) 542-6000



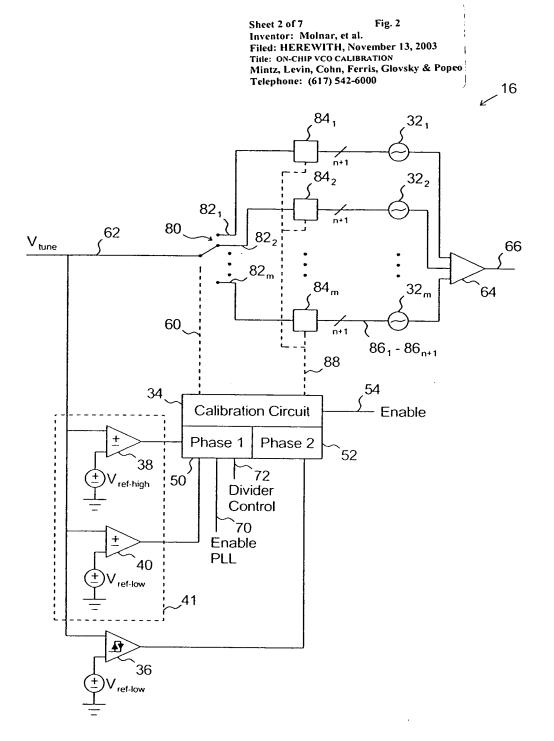


Figure 2

Sheet 3 of 7 Fig. 3
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Telephone: (617) 542-6000

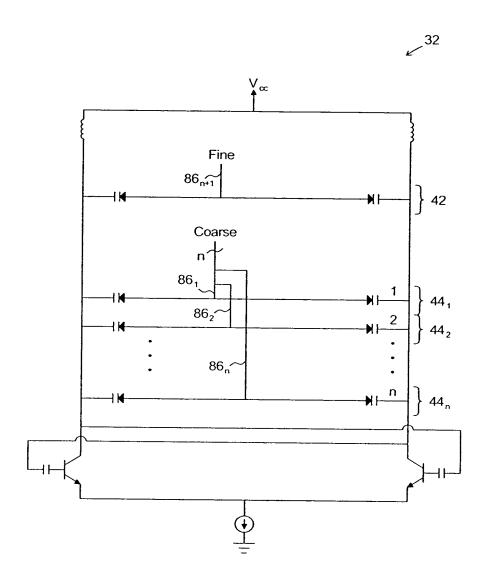


Figure 3

Sheet 4 of 7 Fig. 4
Inventor: Molnar, et al.
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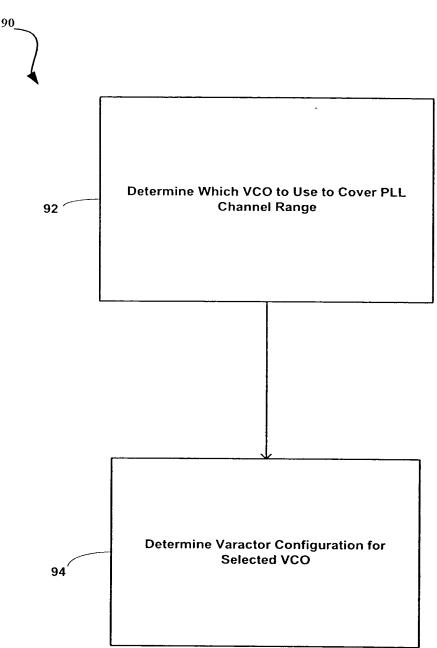


Figure 4

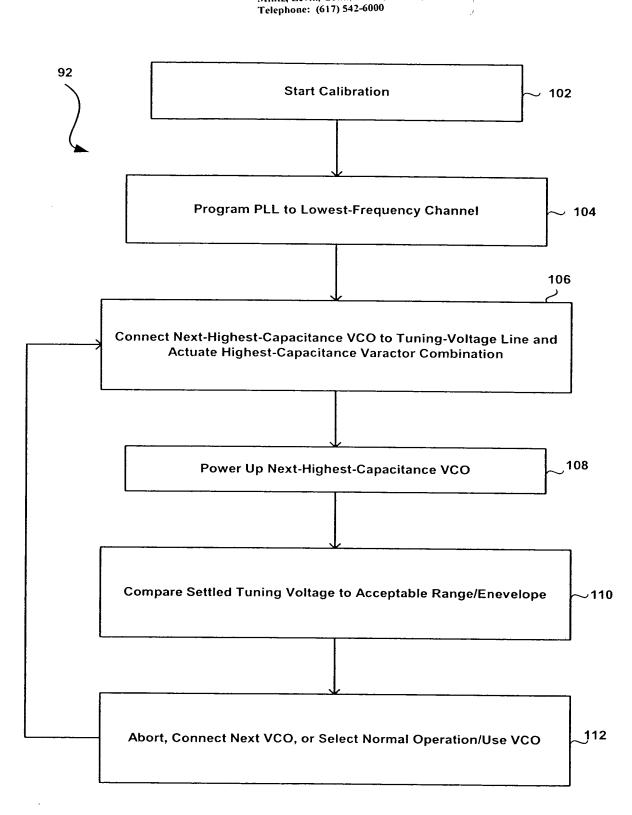


Fig. 5

Sheet 5 of 7

Inventor: Molnar, et al.

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Figure 5

Sheet 6 of 7 Fig. 6
Inventor: Molnar, et al.
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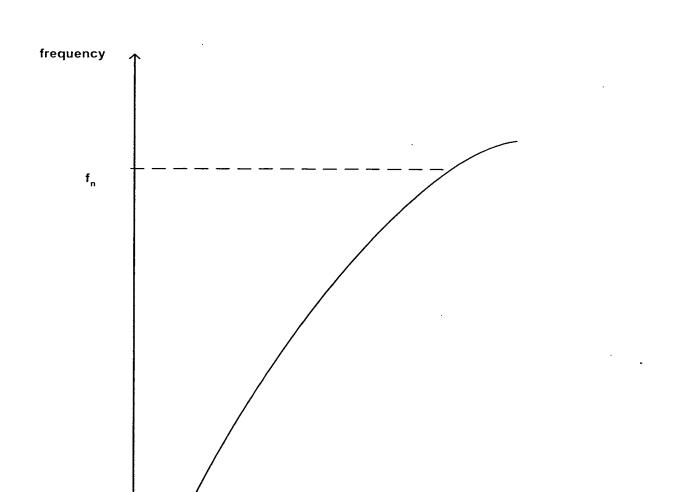


Figure 6

^Vref-high

tuning voltage

Vcc

 $\mathbf{f_i}$

^vref-low

Filed: HEREWITH, November 13, 2003 Title: ON-CHIP VCO CALIBRATION Mintz, Levin, Cohn, Ferris, Glovsky & Popeo Telephone: (617) 542-6000 120 94 Start 122 Power Up and Connect Next-Most-**Capacitive Combination of Varactors of** Normal Operation/Use VCO 124 Set PLL Channel and Allow Tuning Voltage to Settle -126 NO **Hysteresis Output Changes** Sign Or Least-Capacitive **Configuration Reached** YES 128 **Store Varactor** Configuration

Figure 7

Sheet 7 of 7

Inventor: Molnar, et al.